45 V, 1 A PNP medium power transistors Rev. 9 — 13 October 2011

#### 1. **Product profile**

#### **1.1 General description**

PNP medium power transistor series in Surface-Mounted Device (SMD) plastic packages.

#### **Product overview** Table 1.

Type number <sup>[1]</sup>	Package	NPN complement		
	Nexperia	JEITA	JEDEC	
BCP51	SOT223	SC-73	-	BCP54
BCX51	SOT89	SC-62	TO-243	BCX54
BC51PA	SOT1061	-	-	BC54PA

[1] Valid for all available selection groups.

#### 1.2 Features and benefits

- High current
- Three current gain selections
- High power dissipation capability
- Exposed heatsink for excellent thermal and electrical conductivity (SOT89, SOT1061)
- Leadless very small SMD plastic package with medium power capability (SOT1061)
- AEC-Q101 gualified

#### 1.3 Applications

- Linear voltage regulators
- High-side switches
- Battery-driven devices
- Power management
- MOSFET drivers
- Amplifiers

#### 1.4 Quick reference data

#### Table 2. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	-45	V
I <sub>C</sub>	collector current		-	-	-1	А
I <sub>CM</sub>	peak collector current	single pulse; $t_p \le 1 \text{ ms}$	-	-	-2	А

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Table 2.	Quick reference data continued					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
h <sub>FE</sub>	DC current gain	$V_{CE} = -2 V;$ $I_{C} = -150 mA$	63	-	250	
	h <sub>FE</sub> selection -10	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -150 mA	63	-	160	
	h <sub>FE</sub> selection -16	V <sub>CE</sub> = -2 V; I <sub>C</sub> = -150 mA	100	-	250	

### 2. Pinning information

Pin	Description	Simplified outline	Graphic symbol
SOT223			
1	base		
2	collector		2, 4
3	emitter		1
4	collector		3 sym028
SOT89			
1	emitter		
2	collector		2
3	base		3
SOT1061			006aaa231
1	base		
2	emitter	3	3
3 colle	collector		
		1   2     Transparent top view	sym013

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### 3. Ordering information

Table 4. Order	Table 4. Ordering information						
Type number <sup>[1]</sup>	Package	Package					
	Name	Description	Version				
BCP51	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223				
BCX51	SC-62	plastic surface-mounted package; collector pad for good heat transfer; 3 leads	SOT89				
BC51PA	HUSON3	plastic thermal enhanced ultra thin small outline package; no leads; 3 terminals; body $2 \times 2 \times 0.65$ mm	SOT1061				

[1] Valid for all available selection groups.

### 4. Marking

Type number	Marking code
BCP51	BCP51
BCP51-10	BCP51/10
BCP51-16	BCP51/16
BCX51	AA
BCX51-10	AC
BCX51-16	AD
BC51PA	BP
BC51-10PA	BQ
BC51-16PA	BR

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### 5. Limiting values

<b>Table 6.</b> In accordar	Limiting values nce with the Absolute Maximur	n Rating System (IEC	C 60134).		
Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter	-	-45	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-45	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	-5	V
I <sub>C</sub>	collector current		-	-1	А
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms	-	-2	A
I <sub>B</sub>	base current		-	-0.3	А
I <sub>BM</sub>	peak base current	single pulse; $t_p \leq 1 ms$	-	-0.3	А
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$			
	BCP51		<u>[1]</u> _	0.65	W
			[2]	1.00	W
			[3]	1.35	W
	BCX51		<u>[1]</u> _	0.50	W
			[2]	0.95	W
			[3]	1.35	W
	BC51PA		<u>[1]</u> _	0.42	W
			[2]	0.83	W
			[3]	1.10	W
			<u>[4]</u> _	0.81	W
			[5]	1.65	W
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-55	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

[4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.

[5] Device mounted on an FR4 PCB, 4-layer copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

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### 6. Thermal characteristics

Table 7.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air				
	BCP51		<u>[1]</u> -	-	192	K/W
			[2] _	-	125	K/W
			[3]	-	93	K/W
	BCX51		<u>[1]</u> -	-	250	K/W
BC51PA			[2] _	-	132	K/W
			[3]	-	93	K/W
	BC51PA		<u>[1]</u> -	-	298	K/W
			[2] _	-	151	K/W
			[3]	-	114	K/W
			<u>[4]</u> _	-	154	K/W
			[5] _	-	76	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point					
	BCP51		-	-	16	K/W
	BCX51		-	-	16	K/W
	BC51PA		-	-	20	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

[4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.

[5] Device mounted on an FR4 PCB, 4-layer copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

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### 7. Characteristics

#### Table 8. Characteristics

 $T_{amb} = 25 \ ^{\circ}C$  unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base	$V_{CB} = -30 \text{ V}; I_E = 0 \text{ A}$		-	-	-100	nA
	cut-off current	$\label{eq:VCB} \begin{array}{l} V_{CB} = -30 \text{ V}; \text{ I}_E = 0 \text{ A}; \\ T_j = 150 \ ^\circ\text{C} \end{array}$		-	-	-10	μA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; \text{ I}_{C} = 0 \text{ A}$		-	-	-100	nA
h <sub>FE</sub> DC current gain		$V_{CE} = -2 V$					
		$I_C = -5 \text{ mA}$		63	-	-	
		I <sub>C</sub> = -150 mA		63	-	250	
		I <sub>C</sub> = -500 mA	<u>[1]</u>	40	-	-	
	DC current gain	$V_{CE} = -2 V$					
	h <sub>FE</sub> selection -10	I <sub>C</sub> = -150 mA		63	-	160	
	h <sub>FE</sub> selection -16	I <sub>C</sub> = -150 mA		100	-	250	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = -500 mA; I <sub>B</sub> = -50 mA	[1]	-	-	-0.5	V
V <sub>BE</sub>	base-emitter voltage	$V_{CE}$ = -2 V; $I_C$ = -500 mA	<u>[1]</u>	-	-	-1	V
C <sub>c</sub>	collector capacitance	$\label{eq:VCB} \begin{array}{l} V_{CB}=-10 \text{ V}; \text{ I}_{E}=\text{i}_{e}=0 \text{ A};\\ \text{f}=1 \text{ MHz} \end{array}$		-	15	-	pF
f <sub>T</sub>	transition frequency	$V_{CE} = -5 \text{ V}; I_C = -50 \text{ mA};$ f = 100 MHz		-	145	-	MHz

[1] Pulse test:  $t_p \le 300 \ \mu s$ ;  $\delta = 0.02$ .

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### 8. Test information

#### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

### 9. Package outline



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### **10. Packing information**

#### Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Package Description			Packing quantity		
			1000	3000	4000
SOT223	8 mm pitch, 12 mm tape and reel		-115	-	-135
SOT89	8 mm pitch, 12 mm tape and reel; T1	[3]	-115	-	-135
	8 mm pitch, 12 mm tape and reel; T3	[4]	-146	-	-
SOT1061	4 mm pitch, 8 mm tape and reel		-	-115	-
	SOT223 SOT89	SOT2238 mm pitch, 12 mm tape and reelSOT898 mm pitch, 12 mm tape and reel; T18 mm pitch, 12 mm tape and reel; T3	SOT2238 mm pitch, 12 mm tape and reelSOT898 mm pitch, 12 mm tape and reel; T18 mm pitch, 12 mm tape and reel; T3[4]	SOT223         8 mm pitch, 12 mm tape and reel         -115           SOT89         8 mm pitch, 12 mm tape and reel; T1         3         -115           8 mm pitch, 12 mm tape and reel; T1         4         -146	I 000         3000           SOT223         8 mm pitch, 12 mm tape and reel; -115         -           SOT89         8 mm pitch, 12 mm tape and reel; T1         3         -115         -           8 mm pitch, 12 mm tape and reel; T1         3         -115         -           8 mm pitch, 12 mm tape and reel; T3         4         -146         -

[1] For further information and the availability of packing methods, see <u>Section 14</u>.

[2] Valid for all available selection groups.

- [3] T1: normal taping
- [4] T3: 90° rotated taping

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### 11. Soldering



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### **12. Revision history**

#### Table 10.Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BCP51_BCX51_BC51PA v.9	20111013	Product data sheet	-	BC636_BCP51_BCX51 v.8
Modifications:	<ul> <li>Deleted typ</li> </ul>	e number BC636		
	<ul> <li>Added Type</li> </ul>	e number BC51PA		
	Section 1 "I	Product profile": updated		
	• Table 6 and	I <u>7</u> : updated according to la	atest measuremen	ts
	• Figure 1 to	<u>9, 15, 17, 18</u> and <u>21</u> : upda	ated	
	<ul> <li>Figure 10 to</li> </ul>	o <u>14</u> : added		
	Section 8 "	Test information": added		
	<ul> <li>Section 11</li> </ul>	"Soldering": added		
	Section 13	"Legal information": update	ed	
BC636_BCP51_BCX51 v.8	20080222	Product data sheet	-	BC636_BCP51_BCX51 v.7
BC636_BCP51_BCX51 v.7	20070629	Product data sheet	-	BC636_BCP51_BCX51 v.6
BC636_BCP51_BCX51 v.6	20060329	Product data sheet	-	BC636_638_640 v.5
				BCP51_52_53 v.5
				BCX51_52_53 v.4
BC636_638_640 v.5	20041011	Product specification	-	BC636_638_640 v.4
BCP51_52_53 v.5	20030206	Product specification	-	BCP51_52_53 v.4
BCX51_52_53 v.4	20011010	Product specification	-	BCX51_52_53 v.3

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### 13. Legal information

#### 13.1 Data sheet status

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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Product data sheet

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Quick reference data - The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

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