

# PESD15VL2BT

Low capacitance double bidirectional ESD protection diode

13 April 2023 Product data sheet

## 1. General description

Low capacitance double bidirectional ElectroStatic Discharge (ESD) protection diode in a small SOT23 Surface-Mounted Device (SMD) plastic package, designed to protect two signal lines from the damage caused by ESD and other transients.

### 2. Features and benefits

- · ESD protection of two lines
- Max. peak pulse power: P<sub>PPM</sub> = 200 W
- Low clamping voltage: V<sub>CL</sub> = 44 V
- Small SMD plastic package
- Ultra low leakage current: I<sub>RM</sub> = 1 nA
- ESD protection up to 30 kV
- IEC 61000-4-2, level 4 (ESD)
- IEC 61000-4-5 (surge); I<sub>PPM</sub> = 5 A

## 3. Application information

- · Computers and peripherals
- · Audio and video equipment
- · Cellular handsets and accessories
- Communication systems
- Portable electronics
- · SIM card protection

#### 4. Quick reference data

#### Table 1. Quick reference data

| Symbol         | Parameter                | Conditions  | Min | Тур | Max | Unit |
|----------------|--------------------------|---|-----|-----|-----|------|
| $V_{RWM}$      | reverse standoff voltage | T <sub>amb</sub> = 25 °C                                  | -   | -   | 15  | V    |
| C <sub>d</sub> | diode capacitance        | f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C | -   | 16  | -   | pF   |



## 5. Pinning information

#### **Table 2. Pinning information**

| Pin | Symbol | Description    | Simplified outline | Graphic symbol  |
|-----|--------|----------------|--------------------|-----------------|
| 1   | K1     | cathode        | ]3                 |                 |
| 2   | K2     | cathode        |                    | к1              |
| 3   | CC     | double cathode | SOT23              | CC CO 006aaa155 |

## 6. Ordering information

#### **Table 3. Ordering information**

| Type number | Package |  |         |
|-------------|---------|--|---------|
|             | Name    | Description  | Version |
| PESD15VL2BT | SOT23   | plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body | SOT23   |

## 7. Marking

#### Table 4. Marking codes

| Type number | Marking code[1] |
|-------------|-----------------|
| PESD15VL2BT | V6%             |

[1] % = placeholder for manufacturing site code

## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol           | Parameter                | Conditions                          |         | Min | Max | Unit |
|------------------|--------------------------|-------------------------------------|---------|-----|-----|------|
| P <sub>PPM</sub> | rated peak pulse power   | t <sub>p</sub> = 8/20 μs            | [1] [2] | -   | 200 | W    |
| I <sub>PPM</sub> | rated peak pulse current |                                     | [1] [2] | -   | 5   | Α    |
| Tj               | junction temperature     |                                     |         | -   | 150 | °C   |
| T <sub>amb</sub> | ambient temperature      |                                     |         | -65 | 150 | °C   |
| T <sub>stg</sub> | storage temperature      |                                     |         | -65 | 150 | °C   |
| ESD maximu       | m ratings                |                                     |         | •   |     |      |
| V <sub>ESD</sub> | electrostatic discharge  | IEC 61000-4-2; contact discharge    | [3] [2] | -   | 30  | kV   |
|                  | voltage                  | MIL-STD-883; human body model (HBM) | [2]     | -   | 10  | kV   |

- [1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC61000-4-5.
- [2] Measured from pin 1 to 3 or 2 to 3.
- [3] Device stressed with ten non-repetitive ESD pulses.

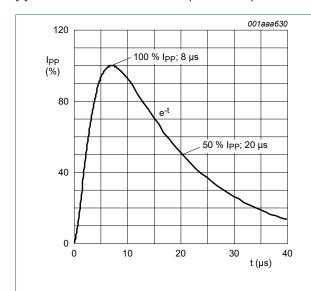


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

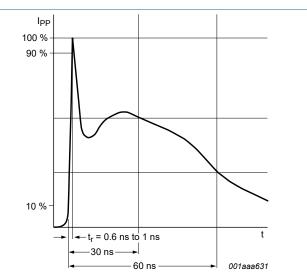


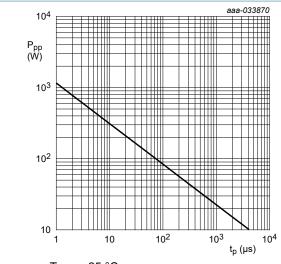
Fig. 2. ESD pulse waveform according to IEC 61000-4-2

### 9. Characteristics

**Table 6. Characteristics** 

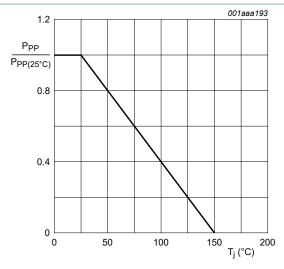
| Symbol            | Parameter                | Conditions  |         | Min  | Тур  | Max  | Unit |
|-------------------|--------------------------|---|---------|------|------|------|------|
| $V_{RWM}$         | reverse standoff voltage | T <sub>amb</sub> = 25 °C                                  |         | -    | -    | 15   | V    |
| $V_{BR}$          | breakdown voltage        | I <sub>R</sub> = 5 mA; T <sub>amb</sub> = 25 °C           |         | 17.1 | 18.8 | 20.3 | V    |
| I <sub>RM</sub>   | reverse leakage current  | V <sub>RWM</sub> = 15 V; T <sub>amb</sub> = 25 °C         |         | -    | 1    | 50   | nA   |
| C <sub>d</sub>    | diode capacitance        | f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C |         | -    | 16   | -    | pF   |
| V <sub>CL</sub>   | clamping voltage         | I <sub>PP</sub> = 1 A; T <sub>amb</sub> = 25 °C           | [1] [2] | -    | -    | 25   | V    |
|                   |                          | I <sub>PPM</sub> = 5 A; T <sub>amb</sub> = 25 °C          | [1] [2] | -    | -    | 44   | V    |
| R <sub>diff</sub> | differential resistance  | I <sub>R</sub> = 1 mA; T <sub>amb</sub> = 25 °C           |         | -    | -    | 225  | Ω    |

- [1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC61000-4-5.
- [2] Measured from pin 1 to 3 or 2 to 3.



T<sub>amb</sub> = 25 °C





ig. 4. Relative variation of peak pulse power as a function of junction temperature; typical values

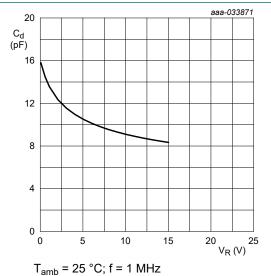


Fig. 5. Diode capacitance as a function of reverse voltage; typical values

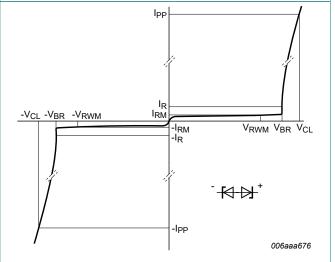
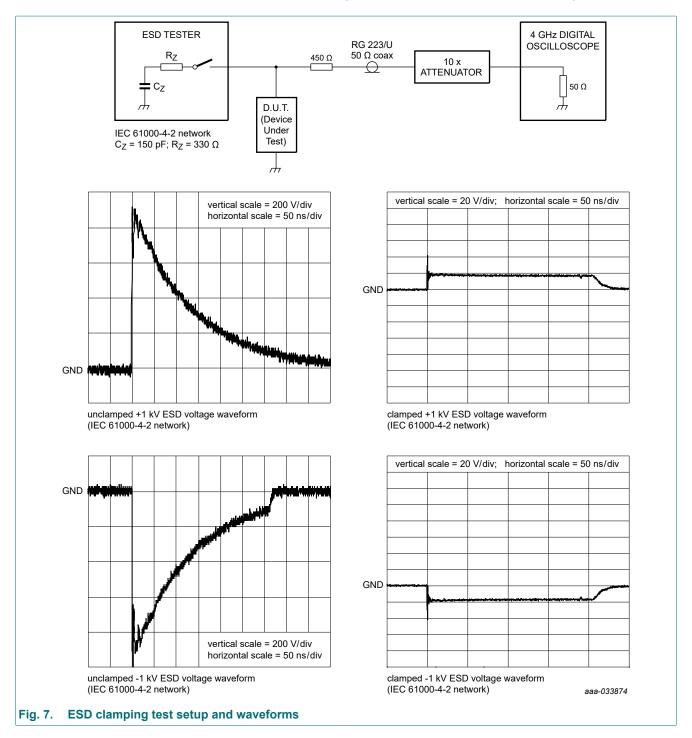


Fig. 6. V-I characteristics for a bidirectional ESD protection diode

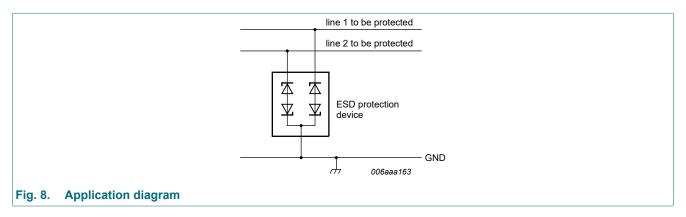
### Low capacitance double bidirectional ESD protection diode



#### Low capacitance double bidirectional ESD protection diode

## 10. Application information

The device is designed for the protection of two bidirectional signal lines from the damage caused by ESD and surge pulses. The device may be used on lines where the signal polarities are above and below ground.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

## 11. Package outline

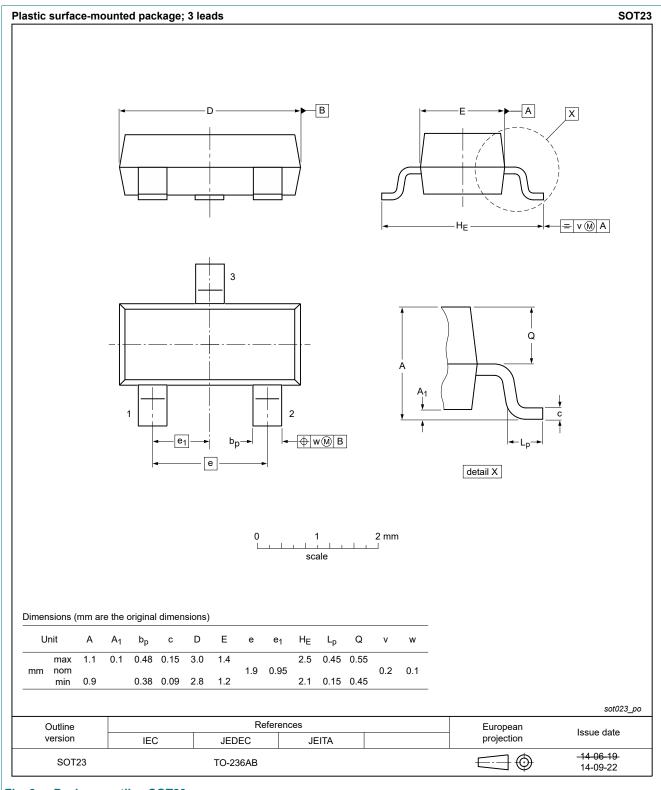
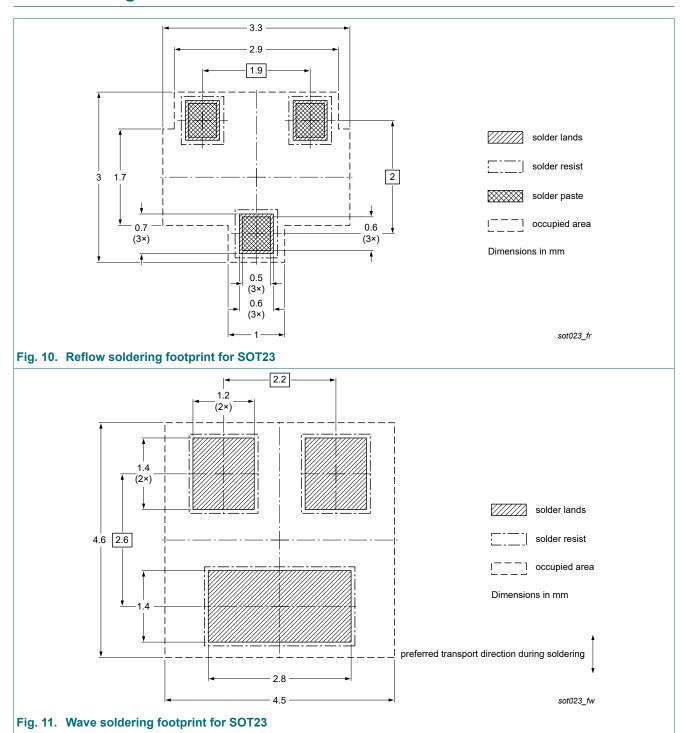


Fig. 9. Package outline SOT23

### Low capacitance double bidirectional ESD protection diode

## 12. Soldering



### Low capacitance double bidirectional ESD protection diode

## 13. Revision history

#### **Table 7. Revision history**

| Table 7. Revision mate | 31 <b>y</b>               |                    |               |  |
|------------------------|---------------------------|--------------------|---------------|--|
| Data sheet ID          | Release date              | Data sheet status  | Change notice | Supersedes   |
| PESD15VL2BT v.3        | 20230413                  | Product data sheet | -             | PESDXL2BT_SER_2  |
| Modifications:         | Nexperia Family data shee | -                  | ta sheet      | the identity guidelines of nexperia.com for automotive |
| PESDXL2BT_SER_2        | 20090825                  | Product data sheet | -             | PESDXL2BT_SER_1  |
| PESDXL2BT_SER_1        | 20051101                  | Product data sheet | -             | -  |

#### Low capacitance double bidirectional ESD protection diode

## 14. Legal information

#### **Data sheet status**

| Document status [1][2]         | Product<br>status [3] | Definition  |
|--------------------------------|-----------------------|---|
| Objective [short] data sheet   | Development           | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification         | This document contains data from the preliminary specification.                       |
| Product [short]<br>data sheet  | Production            | This document contains the product specification.                                     |

- Please consult the most recently issued document before initiating or completing a design.
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